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PATENT

JAN 30 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Akhil K. Garlapati et al.

Title: VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA  
EFFECT OF A CMOS BIPOLAR TRANSISTOR

Application No.: 10/813,837

Filed:

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Examiner: Patel, Rajnikant B.

Group Art Unit:

2838

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6047

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Mail Stop Amendment  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450**RESPONSE TO RESTRICTION REQUIREMENT**

This paper is responsive to an Office action dated December 30, 2005, having a shortened statutory period for response set to expire March 30, 2006.